

!

!

!

!

□

!

"

!

!

!

!

CPU

32 ARM Cortex-M0

2KByte iCache

80MHz

512KByte FLASH

ECC 10 10

32KByte SRAM CRC

1KByte Retention RAM

(MPU)

STANDBY 120 (Max.)

PD2 SRAM I/O Max.)

PD1 SRAM 3 (Max.)

PD0 100nA (Max.)

12MHz /11.2896MHz Crystal

ñ 32.768KHz

RC 80MHz

RC 32.768KHz

2 UART , 921600 bps
2 SPI 20 Mbps 1 I2S 1 QUAD/DUAL/Standard SPI
9bit

2 I2C 1.4 Mbps

1 SCC/SCD ISO7816 /

●

1 10bit 1Msps ADC 10 4 2
1

●

30 GPIO

●

1 DMA 6

●

1 32 RTC

●

1 PWM

●

5 32bit

1 24bit SysTick

1

●

USB (CCID) Bootloader

UART Bootloader

●

RSA ECC SM2 DES/3DES AES SM1 SM4
SHA1/SHA224/SHA256/SHA384/SHA512 SM3
TRNG
CRC16

●

Glue Logic Active Layer (MESH) Passive Layer

●

128 UID

●

1.8 ~ 5.5V

-40 ~ 85

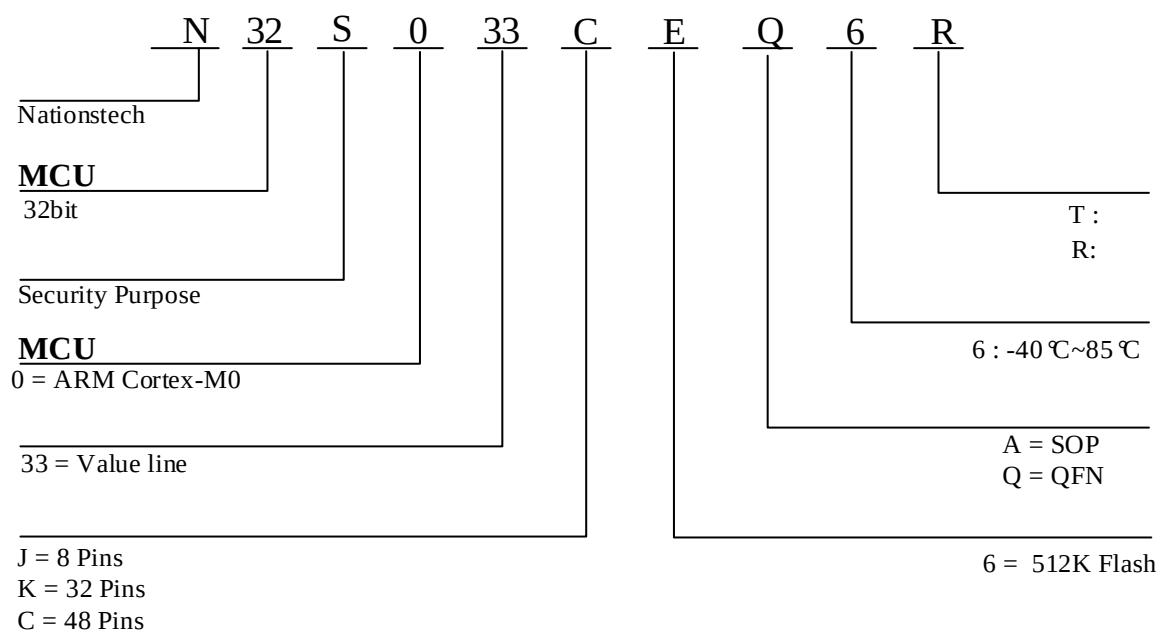
ESD ±4KV HBM / ±1KV CDM

●

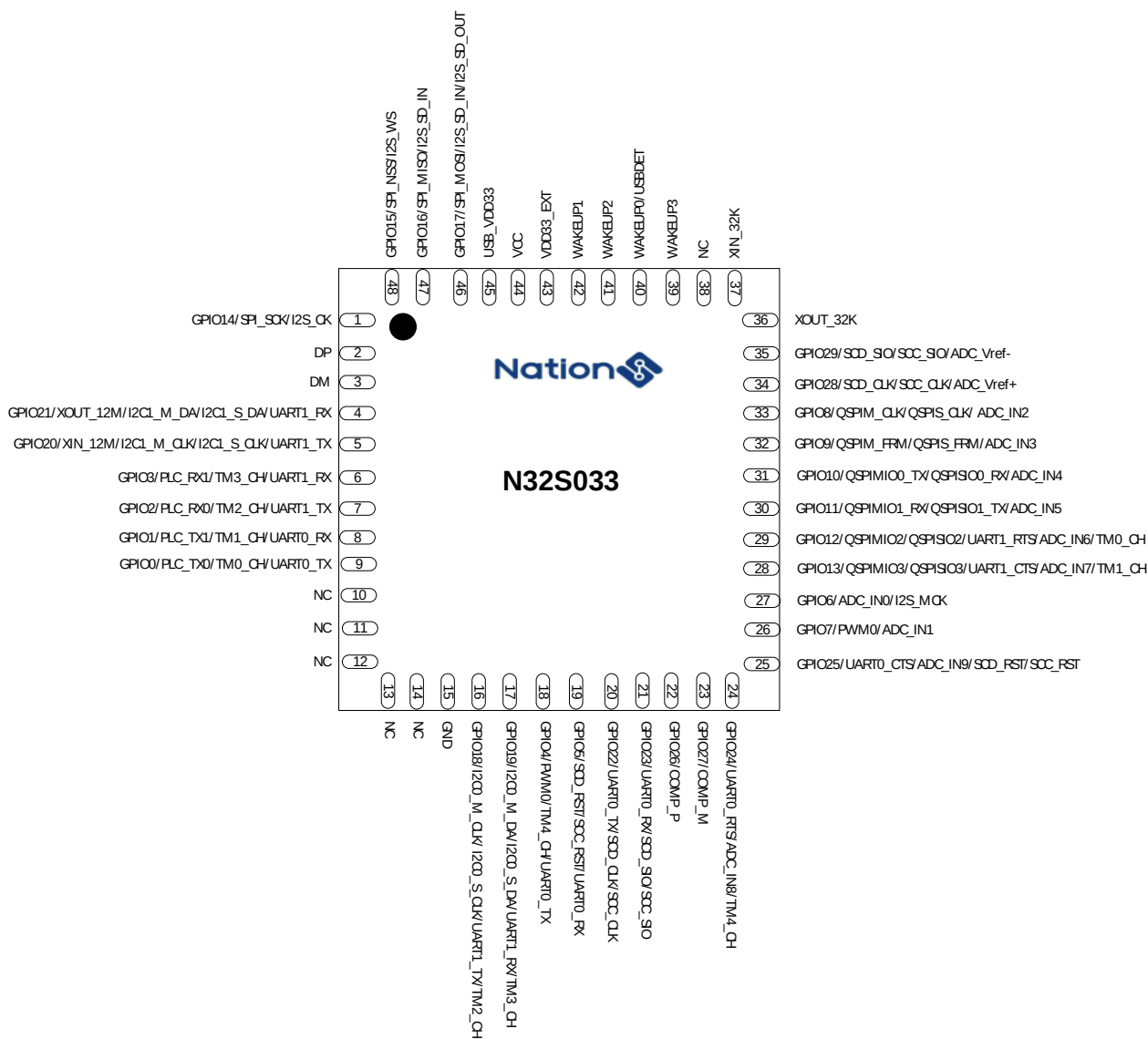
QFN48 (6mm×6mm)

QFN32 (4mm×4mm)

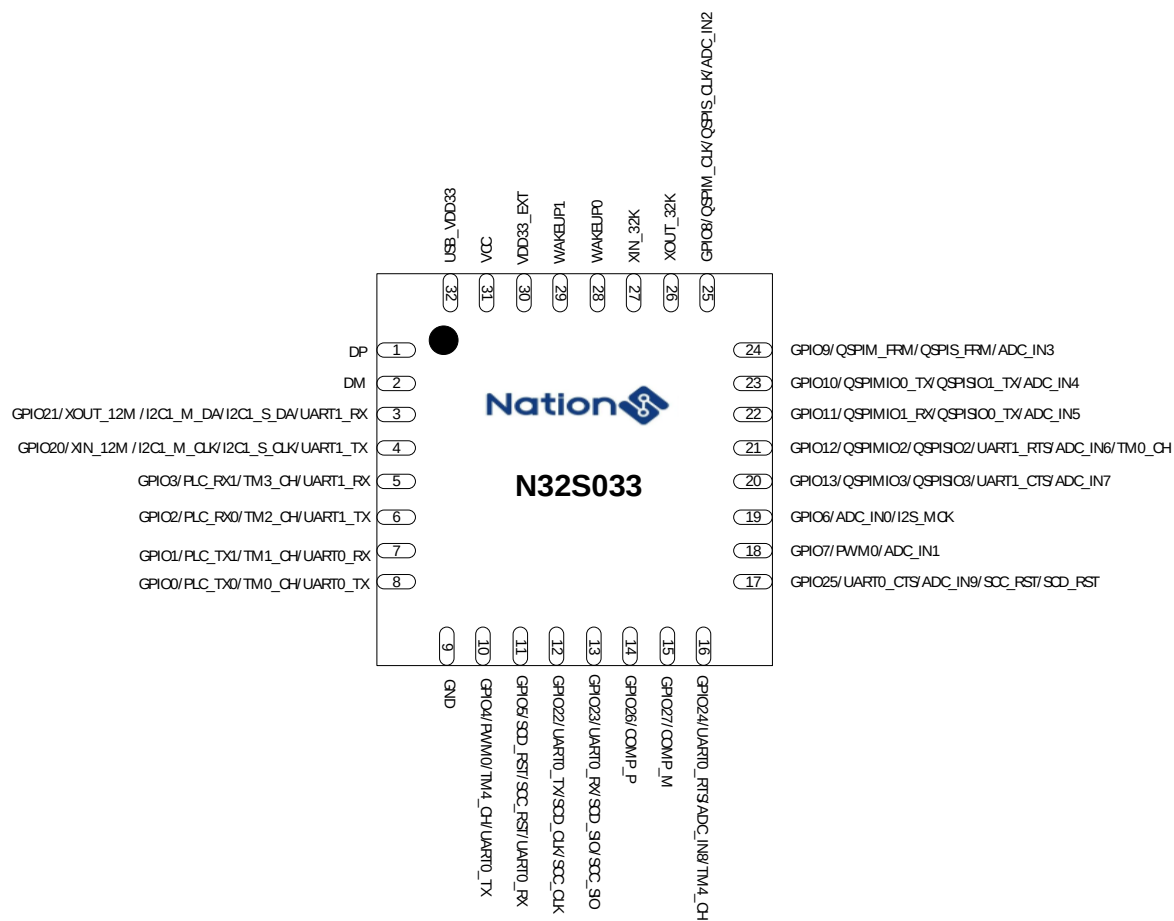
SOP8 (3.9mm×4.9mm)

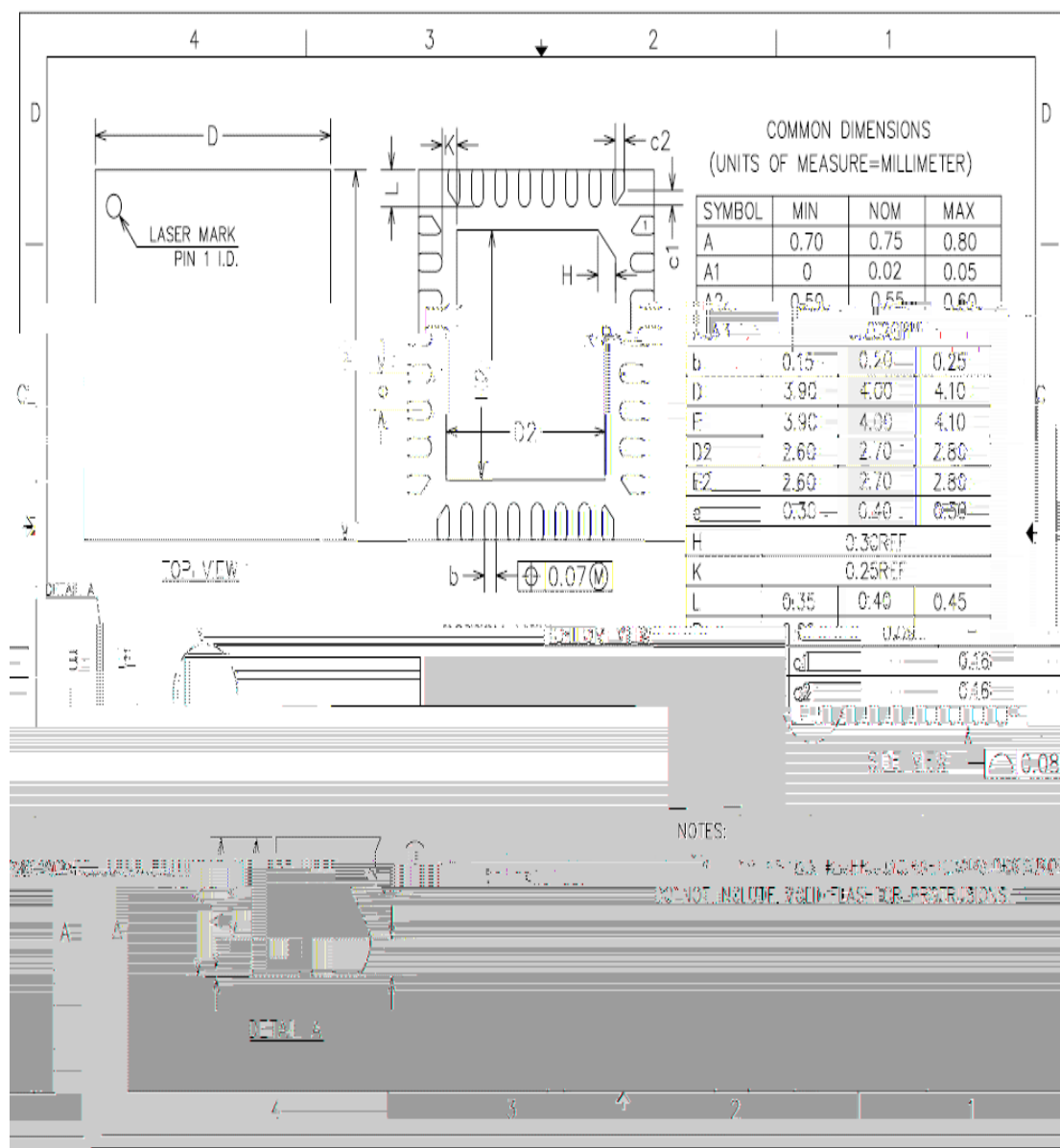


		N32S033	
FLASH	KB	512	
SRAM	KB	32+1	
CPU		ARM Cortex-M0 @80MHz	
		1.8 ~ 5.5V / -40 ~ 85	
	Timer	5	2
	SysTick	1	
	RTC	1	
	USB	1	0
	SPI/I2S	2/1	1/0

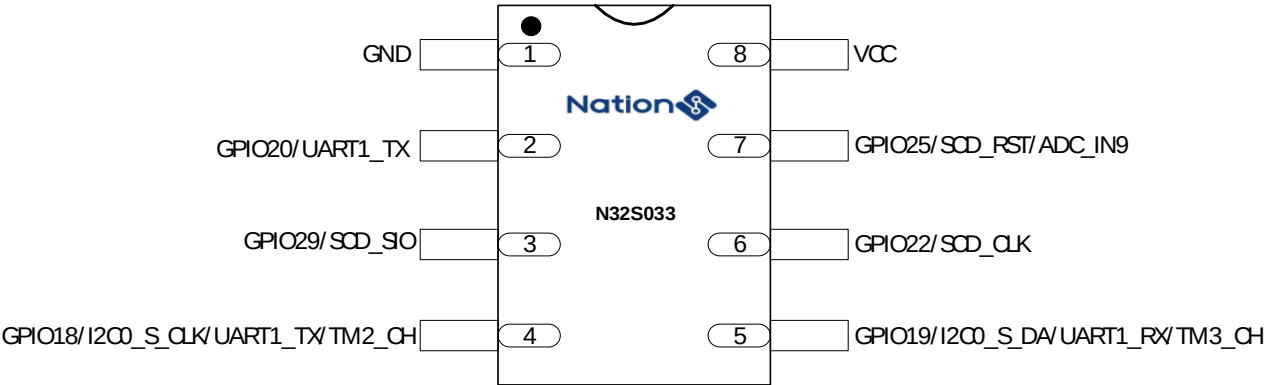




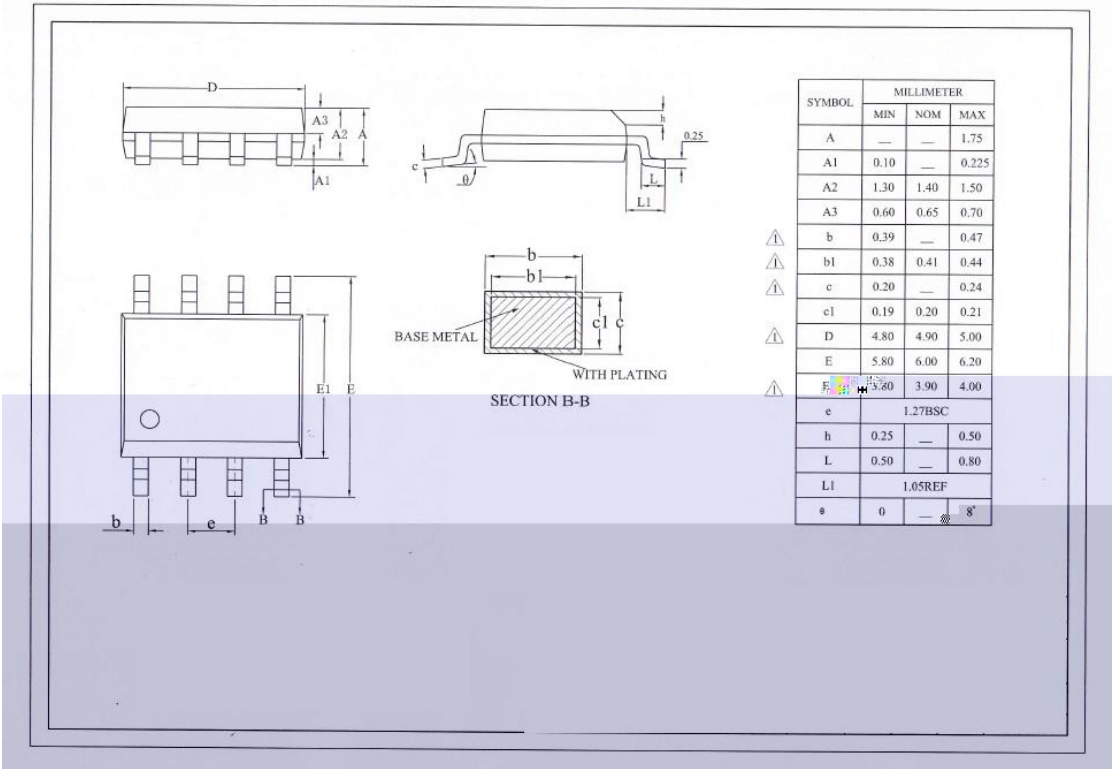




! □
! □



! □



V1.0	2021.7.15	

